

FIG 1

CLASSIFICATION BLOCK DIAGRAM

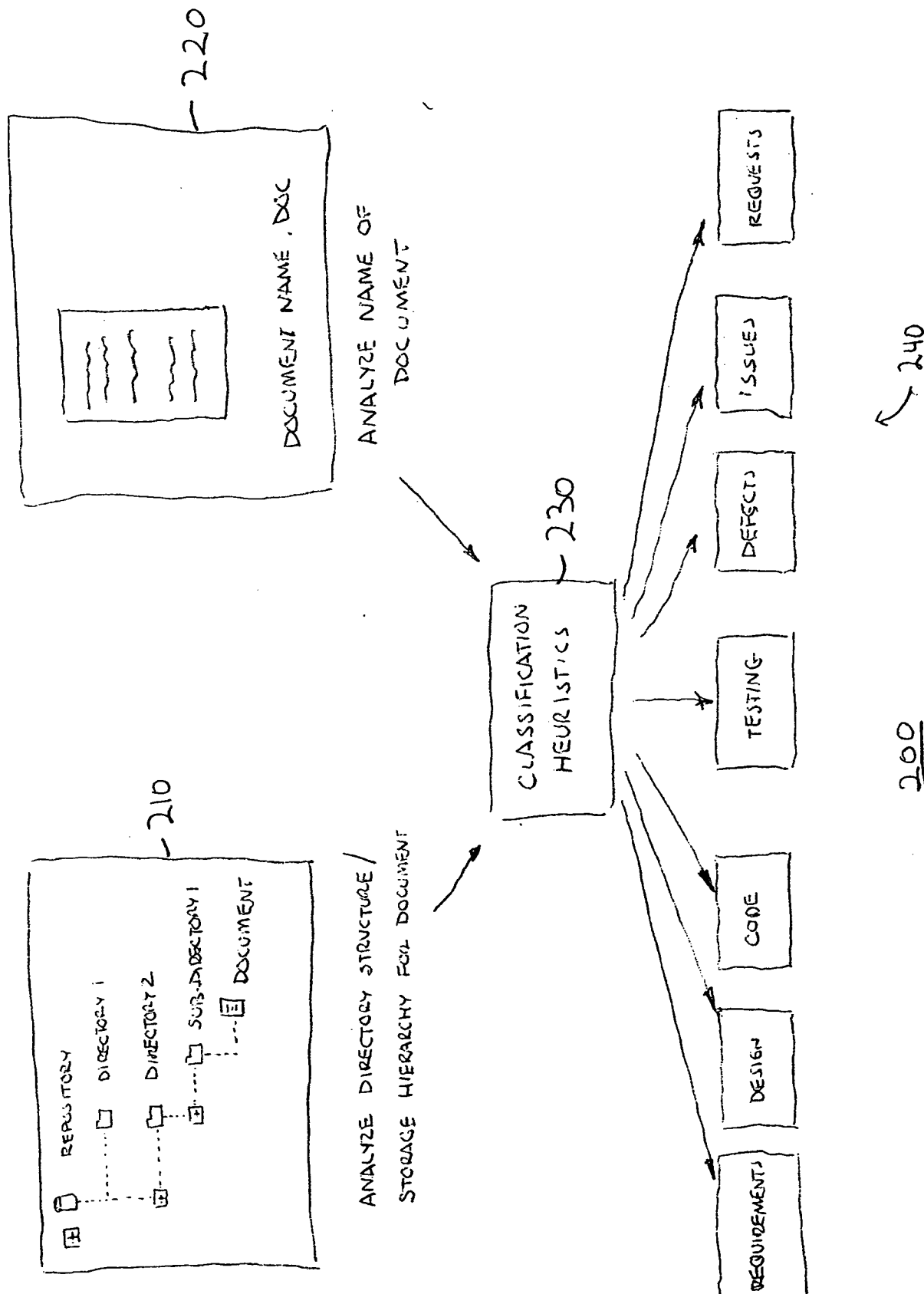
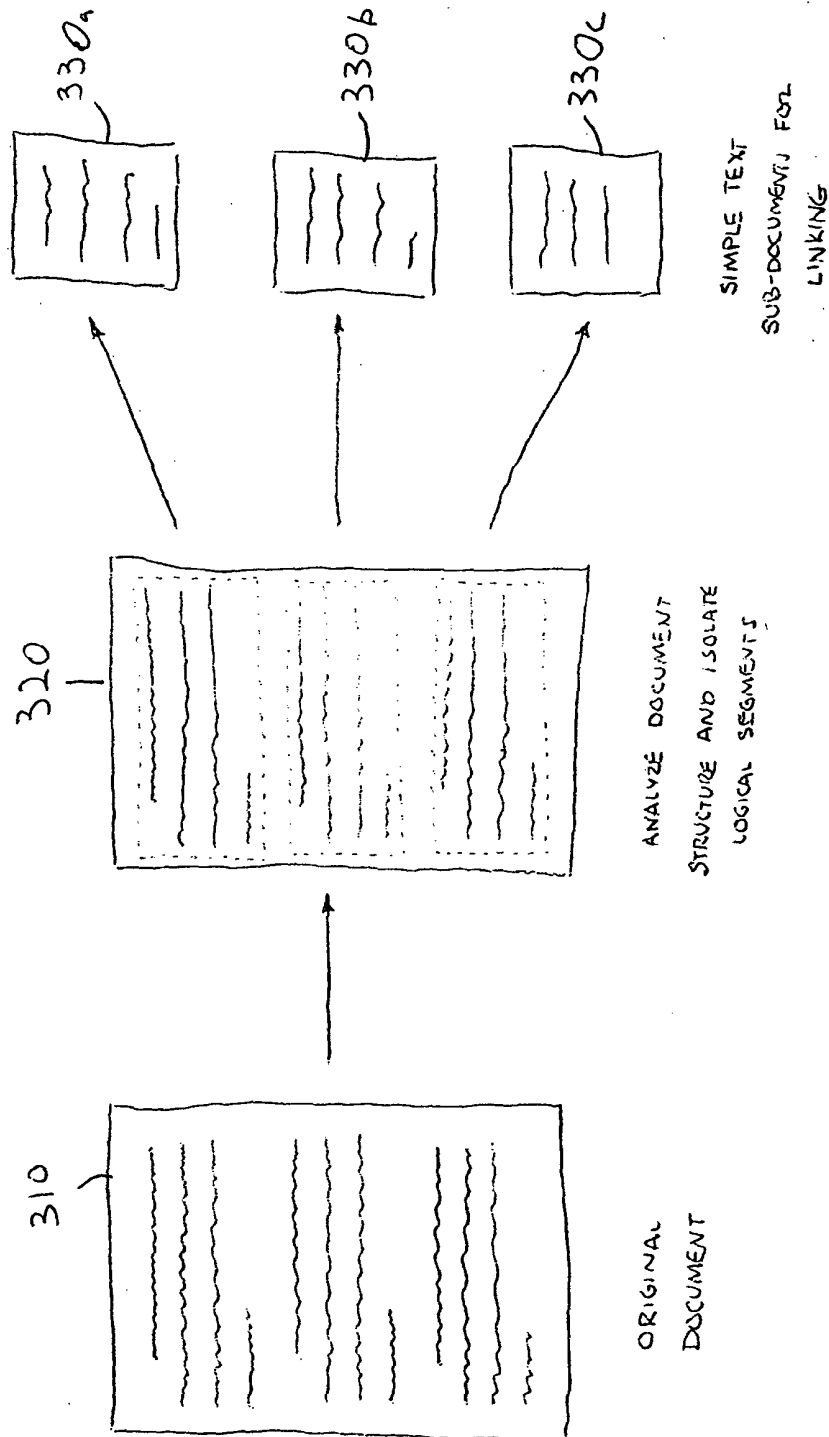


FIG. 2

SEGMENTATION BLOCK DIAGRAM



300

FIG. 3

LINKING BLOCK DIAGRAM

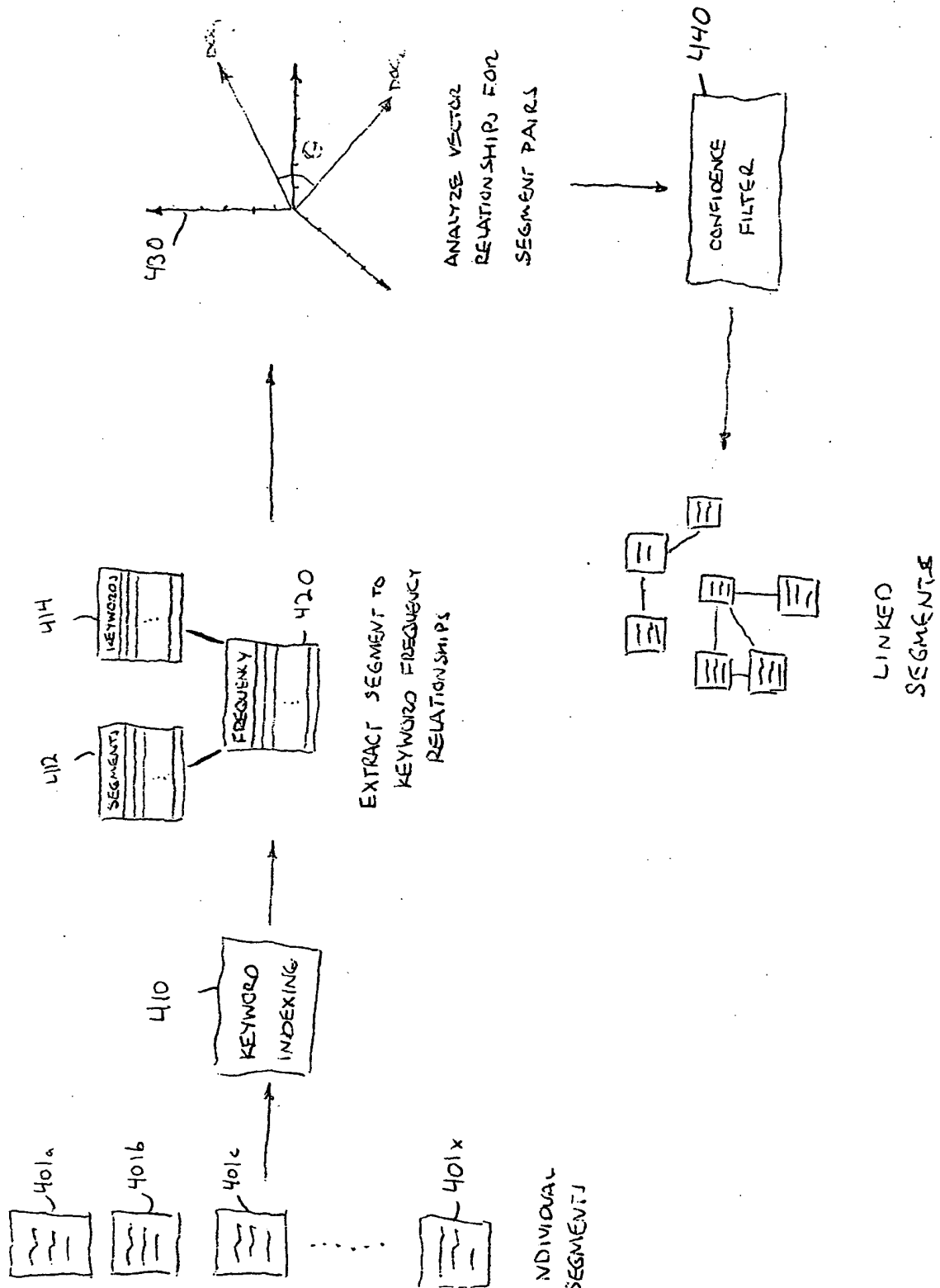


Fig. 4

** TOTAL PAGE.03 **

FRONT-END BLOCK DIAGRAM

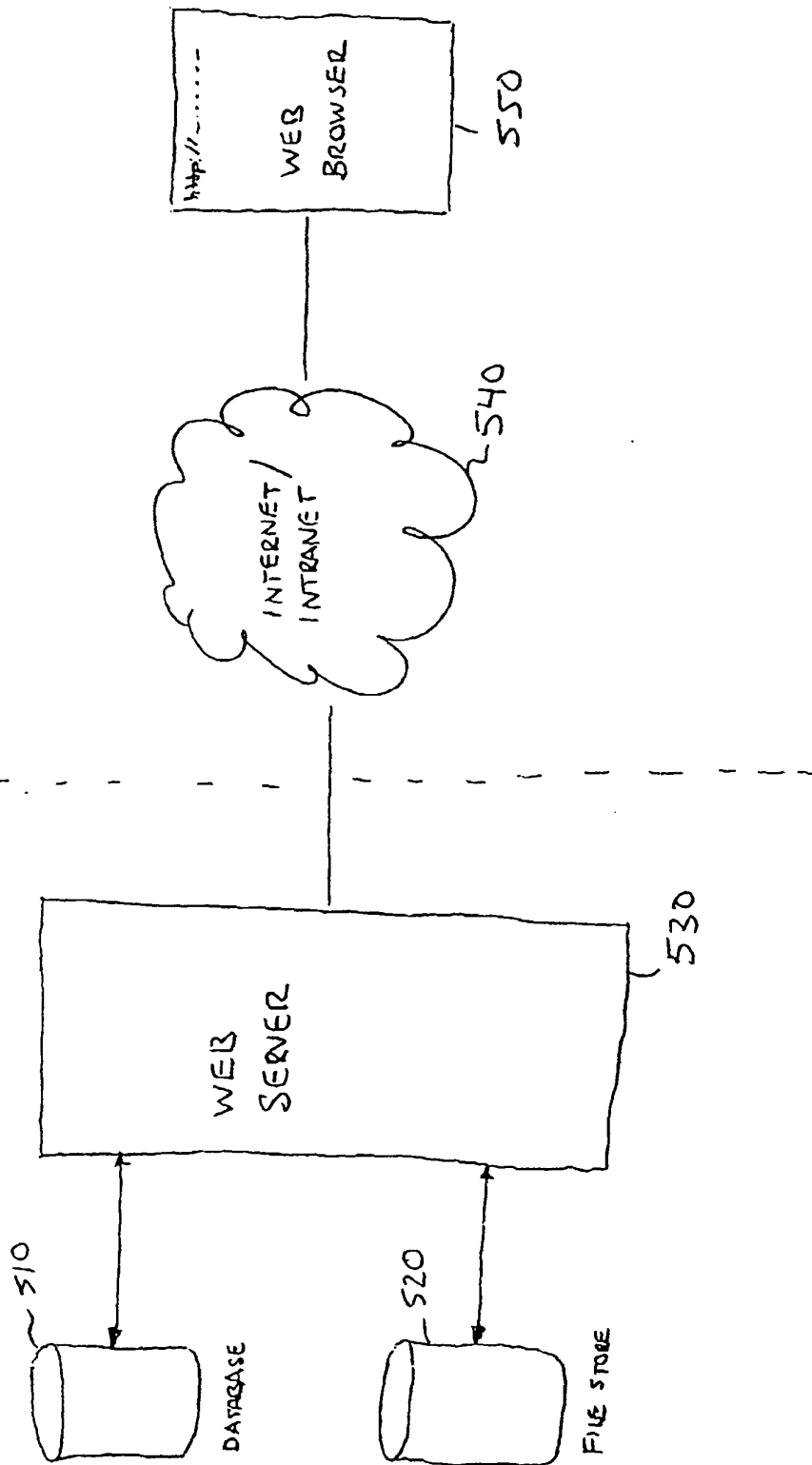
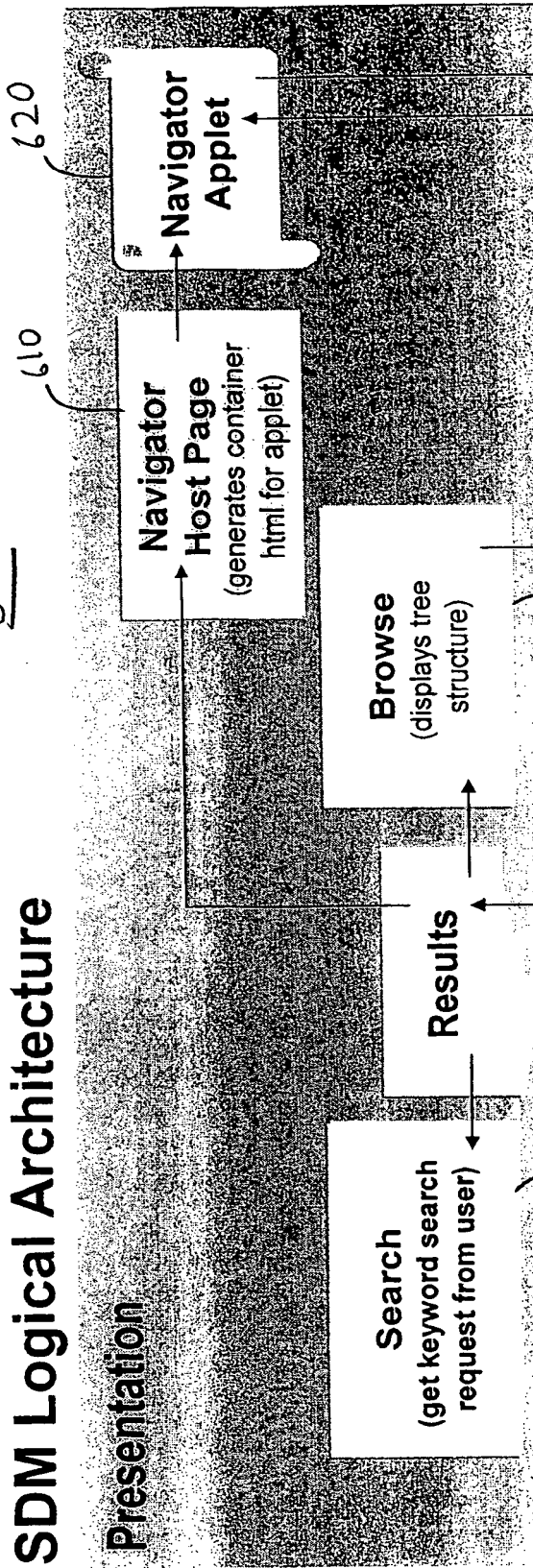


FIG 5

600

SDM Logical Architecture

Presentation



Backend Logic

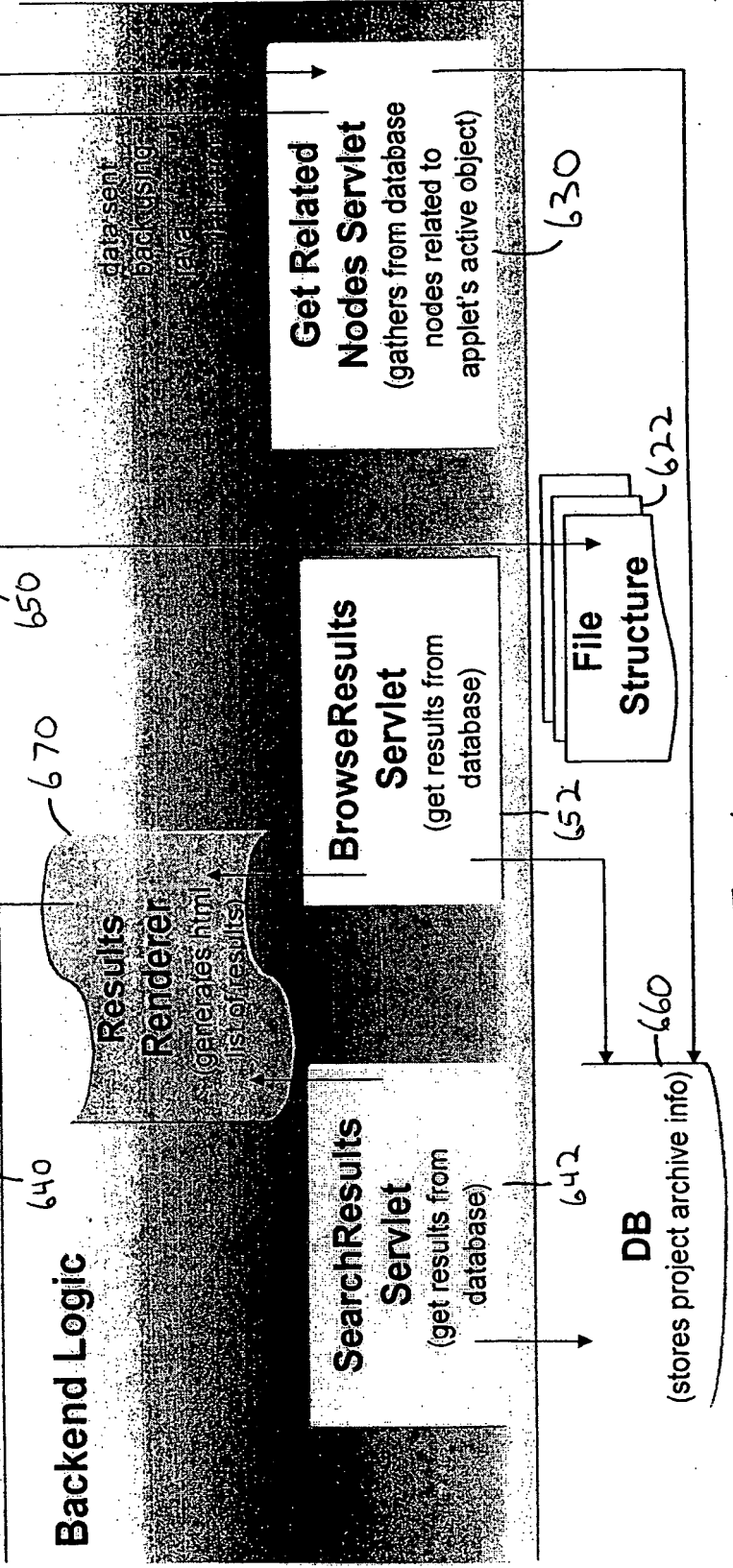


FIG 6

700

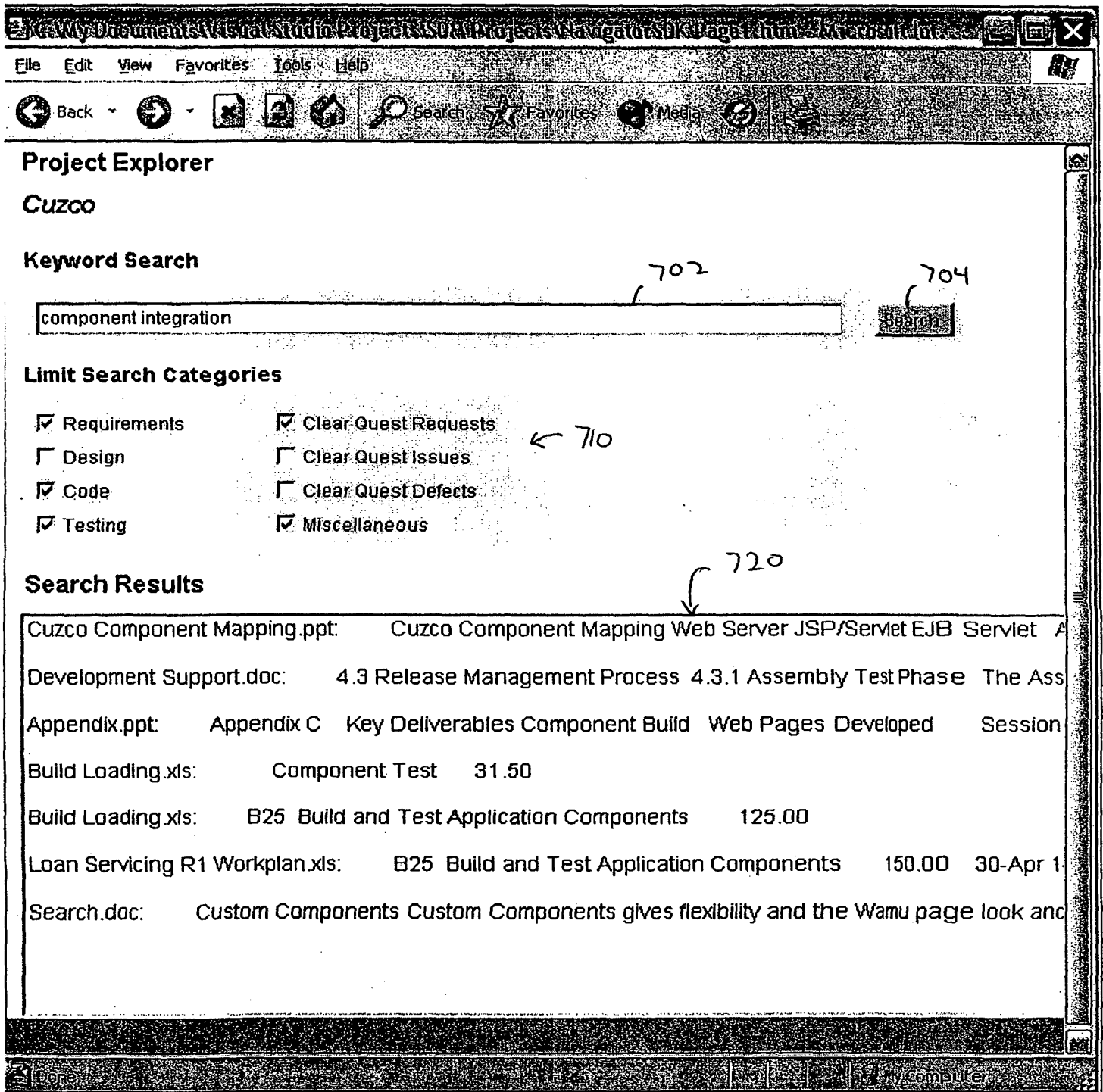


FIG. 7

